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Andrews 8-1-3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): D.M. Andrews et al.

Case: 8-1-3

Serial No.: 09/827,999

Filing Date: April 6, 2001

Group: 2664

Examiner: Chuong Ho

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Chuong Ho Date: July 5, 2006

Patent No.: 7,065,046

Issued: June 20, 2006

Title: Scalable Weight-Based Terabit
Switch Scheduling Method

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Certificate of Corrections Branch

Sir:

Submitted herewith are the following documents relating to the above-identified patent:

- (1) Request for Certificate of Correction for PTO Mistake Under 37 C.F.R. §1.322(a);
- (2) Certificate of Correction; and
- (3) Form PTO-1449.

Certificate

JUL 12 2006

of Correction

JUL 13 2006

There is no additional fee due in conjunction with this Certificate of Correction. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,



Robert W. Griffith
Reg. No. 48,956
Attorney for Applicant(s)
Ryan, Mason & Lewis, LLP
90 Forest Avenue
Locust Valley, NY 11560
(516) 759-4547

Date: July 5, 2006

JUL 13 2006



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Signature: 78-11 Date: July 5, 2006

Patent No.: 7,065,046
Issued: June 20, 2006

Title: Scalable Weight-Based Terabit
Switch Scheduling Method

REQUEST FOR CERTIFICATE OF CORRECTION OF
PATENT FOR PTO MISTAKE UNDER 37 C.F.R. §1.322(a)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Certificate of Corrections Branch

Sir:

In accordance with 37 C.F.R. §1.322(a), Applicants hereby request the issuance of a Certificate of Correction with respect to an error in the above-identified patent.

The error is set forth on the proposed Certificate of Correction (PTO-1050) submitted herewith.

The error is believed to be a Patent Office mistake, namely, the failure to provide a complete list of cited references on the first page as a result of the Examiner's failure to initial a citation under "Other Documents" in the Information Disclosure Sheet dated July 12, 2001. Accordingly, this certificate should be issued pursuant to 37 C.F.R. §1.322(a).

It is believed that there is no additional fee due in conjunction with this Request for Certificate of Correction. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,



Date: July 5, 2006

Robert W. Griffith
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Page 2 of 2

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTIONPage 1 of 1

PATENT NO. : 7,065,046

APPLICATION NO.: 09/827,999

ISSUE DATE : June 20, 2006

INVENTOR(S) : D.M. Andrews et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the "References Cited" section (56) on the first page:

Please insert the following:

--Scheduling Algorithms for Input-Queued Cell Switches by Nicholas William McKeown - thesis submitted in the Graduate Division of University of California, Berkley - 1995--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Robert W. Griffith

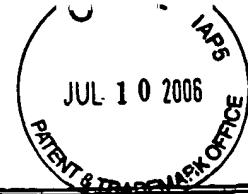
Ryan, Mason & Lewis, LLP

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Locust Valley, New York 11560

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



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Sheet 1 of 1

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. 554-255 (Andrews 8-1-3)

SERIAL NO.09/827,999

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use several sheets if necessary)

APPLICANT Daniel M. Andrews, et al.

RECEIVED

FILING DATE April 6, 2001

JUL 17 2001

GROUP 2661

Technology Center 2600

U.S. PATENT DOCUMENT

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CH	5 2 5 5 2 6 5	Oct. 19, 1993	Eng et al.			
CH	5 2 9 9 1 9 0	Mar. 29, 1994	LaMaire et al.			
CH	5 5 0 0 8 5 3	Mar. 19, 1996	McKeown			
CH	5 8 3 1 9 8 3	Nov. 3, 1998	Varma et al.			
CH	5 9 5 9 9 9 3	9/28/1999	Varma et al.			
CH	6 0 7 2 7 7 2	Jun. 6, 2000	Charny et al.			
CH	6 1 2 8 6 6 6	Oct. 3, 2000	Muller et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
				CLASS	SUBCLASS

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CH	Scheduling Algorithms for Input-Queued Cell Switches by Nicholas William McKeown - thesis submitted in the Graduate Division of University of California, Berkeley - 1995

EXAMINER CHUONG HO DATE CONSIDERED 11/25/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

JUL 13 2006